IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not assigned

Masahiko ANDO et al.

Examiner: Not assigned

Serial No:

Not assigned

Filed: February 25, 2004

For:

Electrode Substrate, Thin Film Transistor, Display

Device and Their Production

TRANSMITTAL OF INFORMATION DISCLOSURE **STATEMENT**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

Date: February 25, 2004

Registration No. 41,232

Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900 Los Angeles, California 90071

Facsimile: 213-337-6701

Telephone: 213-337-6700

| FORM PTO-1449 | | | Docket Number (Optional) 83388.0017 | | Application Number Not assigned | | |
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| INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary) | | | Applicant Masahiko ANDO et al. | | | | |
| | | | Filing Date February 25, 2004 | | Group Art Unit Not assigned | | |
| - | | U.S. PATEN | T DOCUMENTS | | | | |
| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE | |
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| | Nikkei Electronics, June 17, 2002, p | р. 67-78 | • | · - · | | | |
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